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APPLICATION NO.		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/612,990		07/07/2003	Kenji IIda	010194A	1625
23850	7590	05/19/2005		EXAM	INER
ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP 1725 K STREET, NW				NORRIS, JEREMY C	
SUITE 1000	•	••		ART UNIT	PAPER NUMBER
WASHING	WASHINGTON, DC 20006			2841	
				DATEMAN ED OSHODO	-

DATE MAILED: 05/19/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
	10/612,990	IIDA, KENJI					
Office Action Summary	Examiner	Art Unit					
	Jeremy C. Norris	2841					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
1) Responsive to communication(s) filed on 08 M	arch 2005.						
	action is non-final.						
3) Since this application is in condition for allowar	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
closed in accordance with the practice under E							
Disposition of Claims							
4) Claim(s) 1 and 2 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1 and 2 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement.							
Application Papers							
9) The specification is objected to by the Examine 10) The drawing(s) filed on 07 July 2003 is/are: a) Applicant may not request that any objection to the Replacement drawing sheet(s) including the correction of the oath or declaration is objected to by the Examine 11) The oath or declaration is objected to by the Examine 11.	☑ accepted or b)☐ objected to b drawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	ected to. See 37 CFR 1.121(d).					
Priority under 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) □ All b) □ Some * c) □ None of: 1. □ Certified copies of the priority documents have been received. 2. □ Certified copies of the priority documents have been received in Application No. 09/789,771. 3. □ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.							
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary (Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:						
U.S. Patent and Trademark Office PTOL-326 (Rev. 1-04) Office Act	tion Summary Par	t of Paper No./Mail Date 20050510					

Application/Control Number: 10/612,990

Art Unit: 2841

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1 and 2 are rejected under 35 U.S.C. 102(e) as being anticipated by US 6,465,742 (hereafter Hiraoka).

Examiner notes the limitation that the conductive sections be "regulated by abraiding" is a process limitation in a device claim and is thus only considered to the extent that said process impacts the structure. Moreover, it is well settled that the presence of process limitations in product claims, which product does not otherwise distinguish over the prior art, cannot impart patentability to that product. (*In re Thorpe*, 227 USPQ 964, 966 (Fed. Cir. 1985))

Hiraoka discloses, referring to figure 4A, a multilayer circuit board comprising: a plurality of cable layers (6a,b) each of which includes electric conductive sections (7); a plurality of first insulating layers (8), each of which encloses said electric conductive sections in each cable layer and fills spaces between said electric conductive section sections and post vias (10) electrically connecting said electric conductive sections in one cable layer to those in another cable layer, wherein height of said electric

conductive sections in each cable layer are regulated to be equal to that of said first insulating layer enclosing those electric conductive sections (see col. 21, lines 50-68)[claim 1], further comprising a second insulating layer (8) which is formed to enclose said post vias, wherein height of said post vias are regulated to be equal to that of said second insulating layer (see col. 21, lines 50-68) [claim 2].

Response to Arguments

Applicant's arguments filed 8 March 2005 have been fully considered but they are not persuasive. Applicant asserts that the claimed invention distinguishes over the prior art of record because Hiraoka does not disclose the height of the electric conductive sections in each cable layer being regulated by abraiding. However, as stated above, this limitation is a process limitation in a device claim and is thus only considered to the extent that said process impacts the structure. Moreover, it is well settled that the presence of process limitations in product claims, which product does not otherwise distinguish over the prior art, cannot impart patentability to that product. (*In re Thorpe*, 227 USPQ 964, 966 (Fed. Cir. 1985)). Thus Applicant's traversal of the instant rejection on this ground is deemed unsuccessful.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not

mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeremy C. Norris whose telephone number is 571-272-1932. The examiner can normally be reached on Monday - Friday, 9:30 am - 5:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JCSN

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800